

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) An integrated circuit including an on-board system clock, the integrated circuit including a clock filter having a temperature sensor configured to detect an under and over temperature condition conditions of the integrated circuit, the under temperature condition being that at which the temperature is below a predetermined temperature and the over temperature condition being that at which the temperature is above a predetermined temperature, and to prevent output of the system clock to logic circuitry of the integrated circuit in the event that either of the under and over temperature condition is conditions are detected.
2. (Cancelled)
3. (Cancelled)
4. (Previously Presented) An integrated circuit according to claim 1, wherein the predetermined temperature is selected such that a temperature-related speed of the system clock output that is not due to the clock filter is within a predetermined frequency range.
5. (Original) An integrated circuit according to claim 4, wherein the frequency range is within an operating frequency of some or all of the logic circuitry to which the system clock is supplied.
6. (Previously Presented) An integrated circuit according to claim 4, wherein the predetermined temperature is such that an output of the system clock is prevented from reaching some or all of the logic circuitry prior to race conditions due to low temperature causing unpredictable logical behaviour.